Accurate probe tip measurements on wafers and planar circuits require accurate system calibration to the probe tip end. Calibration eliminates systematic measurement errors caused by the imperfection of the measurement instrument and system accessories (cables, connectors, probe tips, etc.). The quality of calibration significantly depends on the accuracy of calibration standards. Calibration substrates contain highly precise standards providing an exact system calibration over a wide frequency range.

The \(|Z|\) Probe and the CSR family of calibration substrates are developed jointly with Rosenberger Hochfrequenztechnik (www.rosenberger.de), a worldwide leading manufacturer of high-frequency coaxial connectors and accessories with over 30 years of experience in the RF and microwave industry.

**Benefits**

- Laser trimmed LOAD resistor, unique accuracy
- Supports SOLT, SOLR, TRM, TRL methods
- Special alignment elements for all standards
- Four non-dispersive standard types: LOAD, OPEN, SHORT, THRU/LINE
- Suitable for 50 to 150 µm pitches
- GSG footprints

**Characteristics**

<table>
<thead>
<tr>
<th>Material</th>
<th>Alumina</th>
</tr>
</thead>
<tbody>
<tr>
<td>Size</td>
<td>16 x 14.5 mm</td>
</tr>
<tr>
<td>Thickness</td>
<td>635 µm</td>
</tr>
<tr>
<td>Dielectric Constant</td>
<td>(\varepsilon = 10.2)</td>
</tr>
<tr>
<td>Effective Permittivity</td>
<td>5.73</td>
</tr>
<tr>
<td>Effective Velocity</td>
<td>0.43</td>
</tr>
<tr>
<td>Phase Velocity</td>
<td>7.36 ps/m</td>
</tr>
<tr>
<td>THRU Impedance</td>
<td>nominally 50 Ω</td>
</tr>
<tr>
<td>DC accuracy (LOAD)</td>
<td>50 Ω +/- 0.15 Ohm (&lt; 0.3 %)</td>
</tr>
<tr>
<td>Temperature drift</td>
<td>&lt; 0.3 % between (-263^\circ\text{C} \ldots +150^\circ\text{C})</td>
</tr>
<tr>
<td>Maximum Power</td>
<td>0.3 W</td>
</tr>
</tbody>
</table>

**Substrate Elements**

**Standard Name**

Each standard on the substrate has a unique name that is used to address it on the substrate standards map. The standards on the substrate are identified by numbers. Each standard has an index number indicating the row and the column of the standard’s location.

**Standard Type**

Standards on a substrate are divided into two groups: transmission and reflection elements. The reflection elements include OPEN, SHORT and LOAD standards. When calibrating a system, these elements are used to provide different reflection factors: fully matched (for Load) or highly reflective (for Open and Short).

Transmission elements are the THRU and LINE standards. They are used for measurements of transmission parameters, providing a physical connection between at least two ports of the system. The electrical length of a transmission element is defined in pico-seconds. THRU standards are the shortest lines on the substrate. The length of the THRU standard is equal to the spacing of all reflective standards. LINE standards are the longer transmission elements and used as LINE for TRL calibration or verification purposes.
CSR-9 Calibration Substrate

Standard Ports

A standard on the calibration substrate consists of two elements that form a pair. This layout is determined to reduce the calibration time and movement on the substrate with high accuracy measurement. This pair contains two elements formed from the same type. CSR-9 is designed for two-port calibration by using two GSG probes.

![Figure 1. Two port configuration](image)

Location

A map of the standards’ locations is defined to enable automated positioning of the probes on the substrate via calibration software. These locations are defined in (X, Y) coordinates in microns according to a specific reference element which has the reference position (0,0). CSR-9 has individual reference elements for OPEN, SHORT, LOAD, THRU (0,0) and LINE elements to align the different distances without touching the standards.

![Figure 2. Reference standard defines the location of an open standard](image)

Probe Spacing and Length of Standards

The distance between probe tips in contact position with the substrate defines the probe spacing in Figure 3. The double align standards are used to define the different probe spacing for LINE elements. There is an individual reference element for each line to ensure accurate calibration, see Figure 4.

![Figure 3. Probe spacing](image)
The electrical length of a LINE standard is defined by the propagation delay time in ($\rho$ s), which describes the propagation delay for the transmission between two probe tips. This value is calculated from the probe spacing for the transmission elements and the effective velocity of light.

```
\[ \rho = \frac{d}{v_{eff}} \]
```

Figure 4. Different references for different lengths on the substrate

**Recommended Probes and Standard Models**

Calibration Substrate CSR-9 can be used with RF probes with pitches from 50 to 150 µm and a GSG footprint. The IZI Probes are recommended to achieve the best measurement accuracy.

Measured values from the calibration substrate include parasitic effects associated with the standards. In order to make the calibration independent of these effects, a simulation of the electric field patterns of the probe touching the standard is done. The results are called correction values. By using these correction values delivered with each IZI Probe, it is possible to set the calibration plane accurately at the end of the tips. The calibration coefficients delivered with the probes are parallel capacitance for the OPEN, series inductance for SHORT and series inductance for LOAD. These values must be entered into the VNA before manual calibration. Using calibration software, these values are adjusted automatically.

```
\begin{align*}
\text{OPEN:} & \quad \text{C}_0 \quad R_0 = \text{INF} \\
\text{SHORT:} & \quad \text{L}_s \quad R_s = 0 \\
\text{LOAD:} & \quad \text{L}_s \quad R_L = 50
\end{align*}
```

Figure 5. Parasitic elements
CSR-9 Calibration Substrate

Substrate Layout

16 x 14.5 mm

(1) Align Element  (2) OPEN  (3) SHORT  (4) LOAD  (5) THRU/LINE
The LOAD standards are laser trimmed thin-film resistors, specified at $50\Omega$ with certain accuracy. The LOAD standard consists of a pair of resistances, where each is an equivalent resistance of two parallel resistances. The standard and its electrical model is found below, where Table 1 on page 5 includes the values of the resistances and their tolerances. Due to fabrication, one of the LOAD standards may not be present on the substrate.

![Figure 6. Standard model](image)

![Figure 7. Electrical model](image)

Table 1. Load resistances and tolerances

<table>
<thead>
<tr>
<th>Substrate ID:</th>
<th>Port 1</th>
<th>Deviation Port 2</th>
<th>Deviation</th>
</tr>
</thead>
<tbody>
<tr>
<td>Number</td>
<td>R1</td>
<td>R2</td>
<td>R3</td>
</tr>
<tr>
<td>9</td>
<td>500</td>
<td>501</td>
<td>502</td>
</tr>
<tr>
<td>504</td>
<td>505</td>
<td>506</td>
<td>507</td>
</tr>
<tr>
<td>509</td>
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<tr>
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<td>905</td>
<td>906</td>
</tr>
</tbody>
</table>
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